

**Notice of References Cited**

Application/Control No.

10/656,791

Applicant(s)/Patent Under  
Reexamination  
SILVERBROOK, KIA

Examiner

BENIYAM MENBERU

Art Unit

2625

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2001/0051766	12-2001	Gazdzinski, Robert F.	600/309
*	B	US-5,757,432	05-1998	Dulong et al.	348/384.1
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Fujioka et al, "Reconfigurable parallel VLSI processor for dynamic control of intelligent robots", January 1996, IEE Proc.-Comput. Digit. Tech., Vol. 143, No. 1, pp. 23-29
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.